

Serial No. 10/814,803

Attorney Docket No. 01-627

LISTING OF CLAIMS:

1. (Currently amended) A multi-layer printed circuit board comprising:

a resin substrate including a plurality of laminated thermoplastic resin films;

a thin film resistor embedded in the resin substrate; and

an electrode disposed on the thin film resistor,

wherein the thermoplastic resin film includes a conductive pattern made of metallic film, and

wherein the conductive pattern is disposed over or under the electrode, the conductive pattern is disposed nearer to the electrode than any other conductive pattern over or under the electrode, and the conductive pattern covers all of a periphery of the electrode.

2. (Currently amended) The multi-layer printed circuit board according to claim 1,

wherein all of a periphery of the thin film resistor is covered with the conductive pattern disposed over or under the resistor.

3. (Original) The multi-layer printed circuit board according to claim 1,

wherein the thin film resistor is covered with the conductive pattern disposed on a side opposite to the electrode across the thin film resistor.

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4. (Original) The multi-layer printed circuit board according to claim 1,

wherein the thin film resistor has a thickness equal to or thinner than $10\mu\text{m}$.

5. (Original) The multi-layer printed circuit board according to claim 4,

wherein the thin film resistor has the thickness equal to or thinner than $1\mu\text{m}$.

6. (Original) The multi-layer printed circuit board according to claim 1,

wherein the resin substrate includes a plurality of thermoplastic resin films laminated and bonded together.

7. (Original) The multi-layer printed circuit board according to claim 1,

wherein the conductive pattern prevents a fluidized thermoplastic resin from moving toward the thin film resistor when a thermoplastic resin composing the thermoplastic resin film is fluidized.

8. (Currently amended) A multi-layer printed circuit board comprising:

a resin substrate including a plurality of laminated thermoplastic resin films; and

a thin film resistor embedded in the resin substrate,

wherein the thermoplastic resin film includes a conductive

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pattern, which is disposed on a surface of the resin film and made of metallic film,

wherein the resin substrate includes a hole filled with a conductive material, and

wherein the thin film resistor is directly connected to the conductive pattern through the conductive material in the hole, the conductive pattern being disposed over or under the resistor, the conductive pattern being disposed nearer to the thin film resistor than any other conductive pattern over or under the resistor, and through the conductive material in the hole

wherein all of a periphery of the thin film resistor is covered with the conductive pattern.

9. (Original) The multi-layer printed circuit board according to claim 8,

wherein the thin film resistor is covered with the conductive pattern disposed over or under the resistor.

10. (Original) The multi-layer printed circuit board according to claim 8,

wherein the thin film resistor has a thickness equal to or thinner than 10 μm .

11. (Original) The multi-layer printed circuit board according to claim 10,

wherein the thin film resistor has the thickness equal to or thinner than 1 μm .

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12. (Original) The multi-layer printed circuit board according to claim 8,

wherein the resin substrate includes a plurality of thermoplastic resin films laminated and bonded together.

13. (Original) The multi-layer printed circuit board according to claim 8,

wherein the conductive pattern prevents a fluidized thermoplastic resin from moving toward the thin film resistor when a thermoplastic resin composing the thermoplastic resin film is fluidized.

14. (Withdrawn) A method for manufacturing a multi-layer printed circuit board, the method comprising the steps of:

preparing a conductive pattern film by forming a predetermined conductive pattern made of metallic film on a thermoplastic resin film;

preparing a thin film resistor film with an electrode by forming a thin film resistor on the thermoplastic resin film and by forming the electrode on the thin film resistor;

laminating the conductive pattern film on the thin film resistor film with the electrode in order to cover a periphery of the electrode with the conductive pattern disposed over or under the electrode; and

bonding the conductive pattern film and the thin film resistor film with the electrode by heating and pressurizing the

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laminates of the conductive pattern film and the thin film resistor film with the electrode.

15. (Withdrawn) The method according to claim 14,
wherein the thin film resistor is covered with the conductive pattern disposed on a side opposite to the electrode across the thin film resistor.

16. (Withdrawn) The method according to claim 14,
wherein the thin film resistor has a thickness equal to or thinner than 10 μm .

17. (Withdrawn) The method according to claim 14,
wherein the conductive pattern prevents a fluidized thermoplastic resin from moving toward the thin film resistor when a thermoplastic resin composing the thermoplastic resin film is fluidized in the step of bonding the conductive pattern film and the thin film resistor film with the electrode.

18. (Withdrawn) A method for manufacturing a multi-layer printed circuit board, the method comprising the steps of:

preparing a conductive pattern film by forming a predetermined conductive pattern made of metallic film on a thermoplastic resin film;

filling a hole having a bottom with a conductive material, the hole being formed in the conductive pattern film to have the bottom of the conductive pattern;

preparing a thin film resistor film by forming a thin film

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resistor on the thermoplastic resin film;

laminating the conductive pattern film on the thin film resistor film in order to connect directly the thin film resistor to the conductive pattern with the conductive material filled in the hole with the bottom, which is provided by the conductive pattern disposed over or under the thin film resistor; and

bonding the conductive pattern film and the thin film resistor film by heating and pressurizing the laminates of the conductive pattern film and the thin film resistor film.

19. (Withdrawn) The method according to claim 18,
wherein the thin film resistor is covered with the conductive pattern disposed over or under the resistor.

20. (Withdrawn) The method according to claim 18,
wherein the thin film resistor has a thickness equal to or thinner than 10 μm .

21. (Withdrawn) The method according to claim 18,
wherein the conductive pattern prevents a fluidized thermoplastic resin from moving toward the thin film resistor when a thermoplastic resin composing the thermoplastic resin film is fluidized in the step of bonding the conductive pattern film and the thin film resistor film with the electrode.